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PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| Applicant: | Shinya | Examiner: | Unassigned |
| Serial No: | To be assigned | Art Unit: | Unassigned |
| Filed: | Herewith | Docket: | 14292 |
| For: | SYSTEM AND METHOD FOR VERIFYING HARDWARE DESCRIPTION | Dated: | February 6, 2001 |

Assistant Commissioner for Patents
United States Patent and Trademark Office
Washington, D.C. 20231

1033 U.S. PTO
09/777543
02/06/01

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the following reference, which is also listed on the attached Form PTO-1449, be made of record in the above-identified case.

1. Japanese Laid Open Patent Application JP-A-Heisei No. 10-149382, dated June 2, 1998.

CERTIFICATE OF MAILING BY "EXPRESS MAIL"

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Date of Deposit: February 6, 2001

I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents and Trademarks, Washington, D.C. 20231 on February 6, 2001.

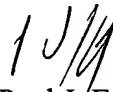
Dated: February 6, 2001


Mishelle Mustafa

Applicant is submitting a copy of the above-cited reference. The relevance of the above-identified reference has been described in the specification, on page 3 thereof.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no petition, certification or fee is required.

Respectfully submitted,



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